



Virtual Secure Engine Firmware Release Notes

For EFR32xG22 and xGM22 Products

Virtual Secure Engine (VSE) firmware is installed with the product. The firmware is upgradable through Gecko Bootloader and Simplicity Studio. Features and security updates available in the latest release are described in this document.

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1 Using This Release

Virtual Secure Engine (VSE) firmware is factory installed with the product.

VSE firmware upgrade images are installed with Gecko SDK Suite in Simplicity Studio. This release contains the following.

- Upgrade image for Gecko Bootloader (.seu file)
- Upgrade application (.hex file)

Upgrade images are signed and encrypted.

Use of the upgrade image with Gecko Bootloader is described in *UG266: Silicon Labs Gecko Bootloader User's Guide, Section on Secure Engine Upgrade*.

To upgrade your device using the Upgrade application, program the .hex file to Flash using Simplicity Commander and then reset the device or use the upgrade feature in Simplicity Studio.

The Secure Engine Manager component in the Gecko SDK Suite provides an API to get the current VSE version from the device. The API reference guide is available on <https://docs.silabs.com/gecko-platform/latest/service/api/group-sl-se-manager>

1.1 Compatible Products

The firmware is compatible with the following Series-2 products:

- EFR32xG22 SoCs
- xGM22 modules

1.2 Support

Development Kit customers are eligible for training and technical support. You can use the Silicon Laboratories web site <https://www.silabs.com/products/development-tools/software> to obtain information about software products and services, and to sign up for product support.

You can contact Silicon Laboratories support at www.silabs.com/support

2 Features and Security Updates

v1.2.7

- Security and stability fixes

v1.2.6

- **Critical** stability fix to prevent devices becoming inoperable under certain conditions. A device whose VSE firmware has been successfully upgraded at least once can become inoperable if the upgrade file is removed from flash after the upgrade and the device is subjected to a large cumulative number of resets. The exact number of reset cycles before failure varies due to process variations and operating temperature but would typically range between 50,000 and 200,000 cycles. **It is imperative that this fix be applied to every device to avoid latent failures.**
- Increase DCI output buffer size to be able to fit attestation tokens for all device configurations.
- Hardened the boot up process against partial RAM retention. If a mailbox command is issued to the VSE and followed by a momentary power failure whose duration is sufficient to cause only a few specific RAM bits to lose state, the POR boot process can terminate in a hard fault.

v1.2.5

- Added VSE mailbox support to enable debug lock. The debug lock status is stored in the status word upon each boot and can be read using the EMLIB API `SE_getConfigStatusBits()`. The SE Manager functions `sl_se_apply_debug_lock()` and `sl_se_get_debug_lock_status()` are also available for EFR32xG22 products from Gecko SDK version 3.0.1.

v1.2.2

- The output mailbox struct is populated with user OTP settings after reset.
- Added an option to manually upgrade the (V)SE firmware when the debug interface is locked and Secure Boot is enabled, as long as the device erase function has not been turned off.
- The error code returned over DCI on failure to validate the firmware during Secure Boot is now more granular as to the reason for failing Secure Boot.
- Security updates

v1.2.1

- Fixed issue that could cause the device to become unrecoverable with both Secure Boot with RTSL and Rollback Prevention enabled

v1.2.0

- Improved handling of fatal errors during chip boot-up sequence
- Access restrictions can now be set on the debug interface. The command `DBG_LOCK_SET_RESTRICTION` is added and status of individual restrictions is added to the status commands.

v1.1.8

- None for this product

v1.1.7

- Security update

v1.1.6

- Added DCDC configuration retention across soft reset

v1.1.5

- Stability fixes

v1.1.4

- Unauthenticated device recover will now also set all RAM words to 0

v1.1.3

- Commands with reserved bits (i.e. unused bits from parameters/options) will now check these bits are set to zero
- Fixed issue that prevented use of the UserData page
- Added workaround for a Cortex-M33 errata when exercising TrustZone debug lock bits
- Aligned DCI success status codes with EFR32xG21 products
- Resolved debug lock issue

v1.1.1 (first release for EFR32xG22 products)

- Fixed an issue where a device with Secure Boot turned on would not be able to be secure unlocked on secure boot failure
- Fixed issue preventing Secure Unlock to function properly on Secure Boot failure

3 Known Issues

- None

4 Legal

4.1 Disclaimer

Silicon Labs intends to provide customers with the latest, accurate, and in-depth documentation of all peripherals and modules available for system and software implementers using or intending to use the Silicon Labs products. Characterization data, available modules and peripherals, memory sizes and memory addresses refer to each specific device, and "Typical" parameters provided can and do vary in different applications.

Application examples described herein are for illustrative purposes only.

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